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## APPLICANTS

Tomio Matsuzaki, Tokyo, JAPAN;

Kazuyoshi Arai, Tokyo, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

*none*  
*Loke*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-324973 11/08/2002

JAPAN 2003-147447 05/26/2003

JAPAN 2003-324204 09/17/2003

*Loke*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 56	TOTAL CLAIMS 35	INDEPENDENT CLAIMS 2
Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature	<i>[Initials]</i> Initials			

## ADDRESS

01933

FRISHAUF, HOLTZ, GOODMAN &amp; CHICK, PC

767 THIRD AVENUE

25TH FLOOR

NEW YORK, NY

10017-2023

## TITLE

Wiring structure on semiconductor substrate and method of fabricating the same

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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